

Serial No. 09/996,714
Amdt. dated March 24, 2004
Reply to Office Action of November 5, 2003

Docket No. K-0343

REMARKS/ARGUMENTS

Claims 1-19 are pending in this application. By this Amendment, the specification and claims 1-3, 6 and 8 are amended and new claims 9-19 are added. Reconsideration in view of the above amendments and following Remarks is respectfully requested.

The Office Action rejects claim 1 under 35 U.S.C. § 102(e) as anticipated by ANH, Y J (2000KR-0038273 – hereinafter “KR ‘273”); and rejects claims 2-8 under 35 U.S.C. § 102(e) as anticipated by Kim, J S (2000KR-0064829 – hereinafter “KR ‘829”). These rejections are respectfully traversed.

Applicants note the references applied in the Office Action (i.e., KR ‘273 and KR ‘829) are not prior art references, because the publication dates of these references are later than the filing date of the present application. That is, the publication date of KR ‘273 is January 16, 2002 and the publication date of KR ‘829 is May 9, 2002. Both of these dates are later than the filing date of the present application, which is November 30, 2001. Thus, the references cited in the outstanding Office Action are not prior art references under any of the paragraphs in 35 U.S.C. § 102. English abstracts including the publication dates of both references are enclosed for the Examiner’s convenience. Accordingly, it is respectfully requested the rejections noted in the outstanding Office Action be withdrawn.

In addition, the claims have been amended to better correspond with U.S. claim drafting practice and have not been amended to overcome any cited art.

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Further, new claims 9-19 have been added to set forth the invention in a varying scope, and it is respectfully submitted the new claims are supported by the originally filed specification. In particular, new independent claim 9 includes a feature which the PDP includes a single trigger electrode between the pair of electrodes. Claims 10-19 are dependent claims.

In addition, the specification has been amended to correct minor informalities. No new matter has been added.

CONCLUSION

In view of the foregoing amendments and remarks, it is respectfully submitted that the application is in condition for allowance. Favorable consideration and prompt allowance are earnestly solicited. If the Examiner believes that any additional changes would place the application in better condition for allowance, the Examiner is invited to contact the undersigned attorney, **David A. Bilodeau**, at the telephone number listed below.

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To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,
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Nov, 2, 2000

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(71) Applicant:

LG ELECTRONICS INC.

(72) Inventor:

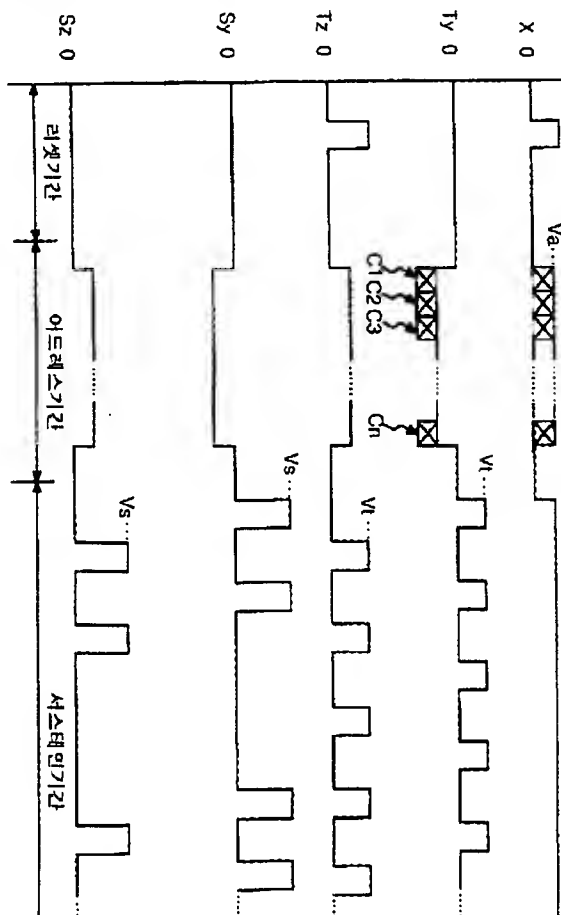
KIM, JAE SEONG

(30) Priority:

(54) Title of Invention

METHOD FOR DRIVING PLASMA DISPLAY PANEL

Representative drawing



(57) Abstract:

PURPOSE: A method for driving a plasma display panel(PDP) is provided, which makes a stable discharge from the first stage of a sustain period and also improves a brightness.

CONSTITUTION: The PDP drives one frame by dividing the frame into five sub fields having different discharge number each other to display a gray level of an image. And each sub field is divided into a reset period to make a uniform discharge, and address period to select a discharge cell and a sustain period displaying the gray level according to the discharge number. During a reset period, a reset pulse is supplied to the second trigger electrode(Tz) of the discharge cell to make a reset

discharge for initializing the discharge cell. A DC voltage is supplied to an address electrode(X) to prevent a mis-discharge. During an address period, a scan pulse(C) is supplied to the first trigger electrode(Ty) in sequence and at the same time a data pulse(Va) synchronized to the scan pulse is supplied to the address electrode. Then, an address discharge is occurred in the discharge cell supplied with the scan pulse. During a sustain electrode, a sustain pulse is applied between the scan/sustain electrode(Sy) and the first trigger electrode and a common sustain electrode(Sz) and the second trigger electrode(Tz).

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July 5, 2000

(71) Applicant:

LG ELECTRONICS INC.

(72) Inventor:

AHN, YEONG JUN

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(54) Title of Invention

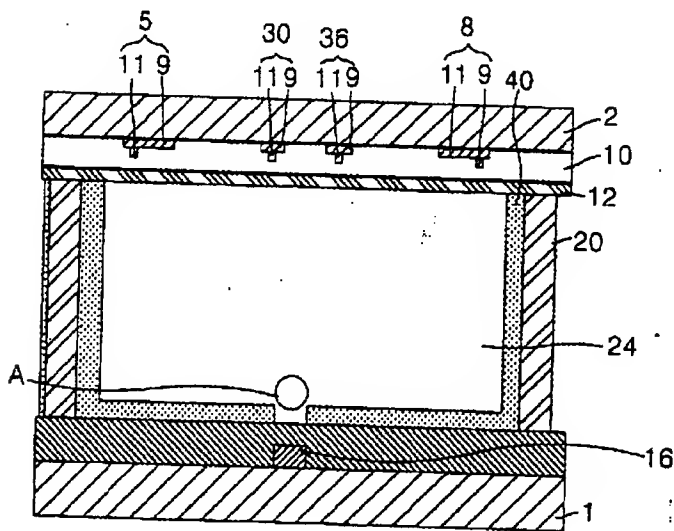
PLASMA DISPLAY PANEL

Representative drawing

(57) Abstract:

PURPOSE: A plasma display panel is provided to restrain a rising voltage applied to an address electrode by using a fluorescent layer for exposing a part of lower dielectric layer formed a lower electrode.

CONSTITUTION: A scan/sustain electrode(5), a common sustain electrode(8), and a couple of trigger electrode(30,36) are formed on an upper substrate(2). The scan/sustain electrode(5), the common sustain electrode(8), and the trigger electrode couple (30,36) are formed with a transparent electrode() and a bus electrode(11), respectively. The trigger electrode couple(30,36) are aligned between the scan/sustain electrode(5) and the common sustain electrode(8). An



upper dielectric layer(10) and a protective layer(12) are laminated on the upper substrate(2). An address electrode(16) is formed on a lower substrate(1). A lower dielectric layer and a barrier (20) are formed on the lower substrate(1). A red, a blue, and a green fluorescent layer(40) are applied on a surface of the lower dielectric layer and a surface of the barrier(20). A groove(A) is formed on the lower dielectric layer. A discharge space(24) is formed between the upper and the lower substrates(2,1) and the barrier(20).

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